

Silicon NPN Power Transistors

2SC3300

DESCRIPTION

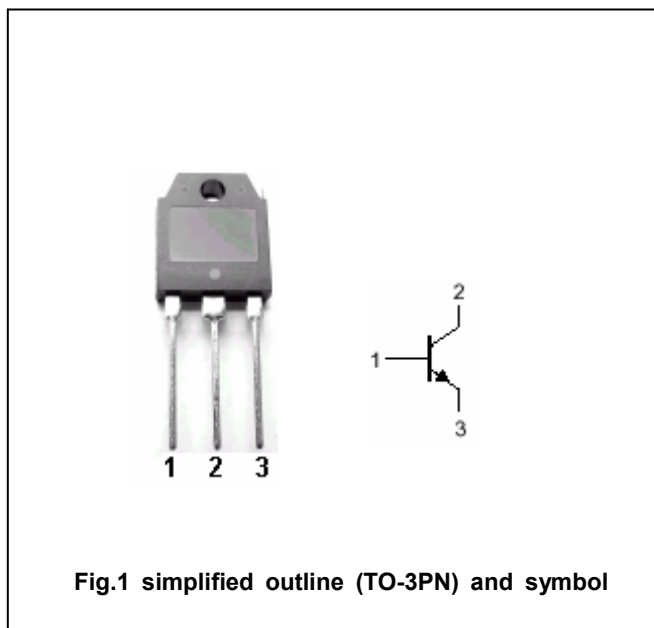
- With TO-3PN package
- Low saturation voltage
- Wide area of safe operation

APPLICATIONS

- Power and general purpose application

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

**ABSOLUTE MAXIMUM RATINGS (TC=25℃)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	100	V
V_{CEO}	Collector-emitter voltage	Open base	100	V
V_{EBO}	Emitter-base voltage	Open collector	6	V
I_C	Collector current (DC)		15	A
I_{CM}	Collector current-peak		25	A
P_C	Collector power dissipation	$T_C=25^\circ\text{C}$	100	W
T_j	Junction temperature		150	℃
T_{stg}	Storage temperature		-55~150	℃

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =25mA ; I _B =0	100			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =1mA ; I _E =0	100			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA ; I _C =0	6			V
V _{CE(sat)}	Collector-emitter saturation voltage	I _C =10A ; I _B =1A			0.5	V
V _{BE(sat)}	Base-emitter saturation voltage	I _C =10A ; I _B =1A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =100V ; I _E =0			0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =6V ; I _C =0			0.1	mA
h _{FE}	DC current gain	I _C =5A ; V _{CE} =4V	30		120	

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PACKAGE OUTLINE

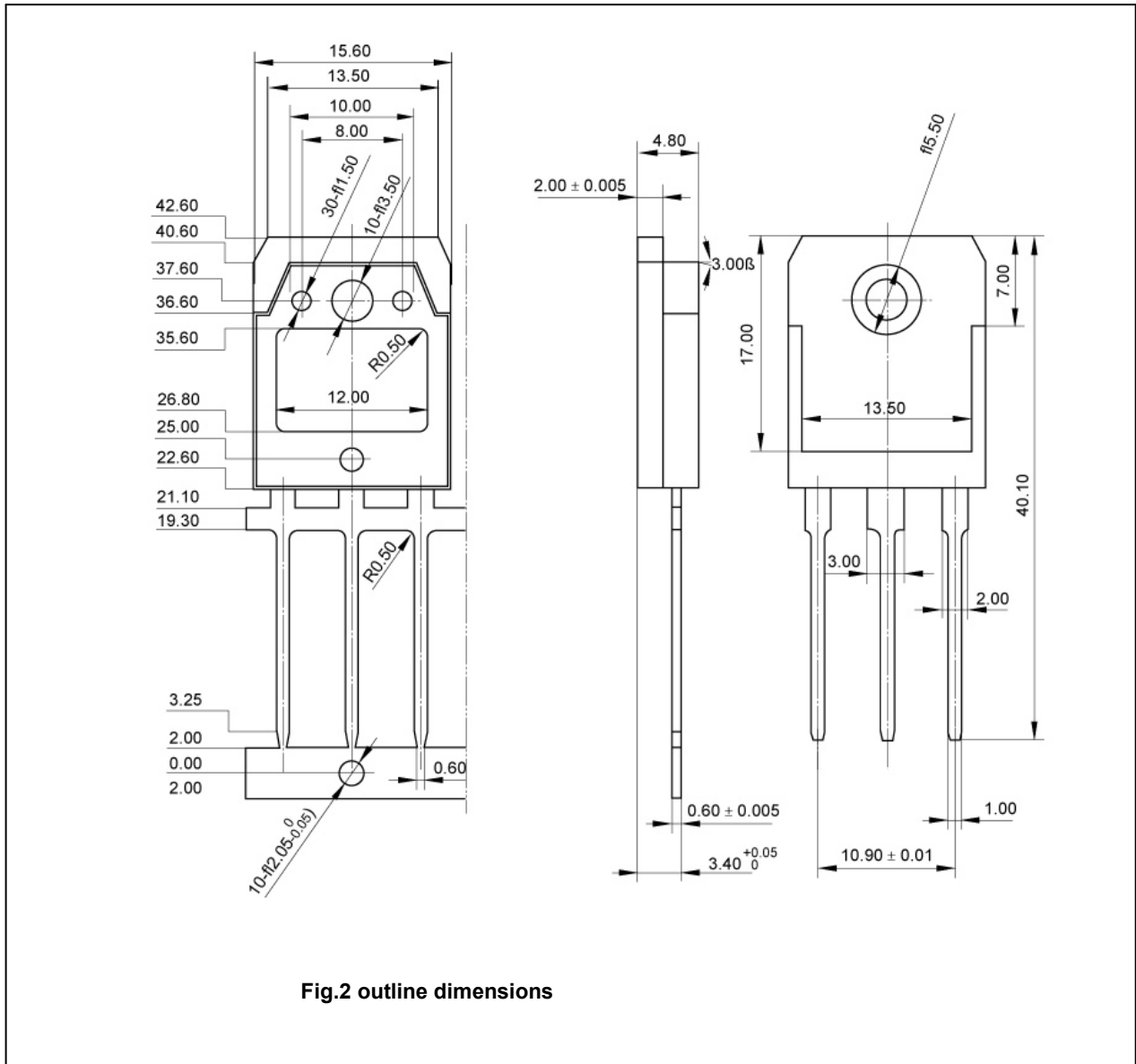


Fig.2 outline dimensions